

## EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	6	microelectronic and owais with jamil.in.	US-PGPUB; USPAT; EPO; JPO	OR	ON	2006/09/08 18:51
L2	40	(print\$3 wiring circuit) near3 (board substrate) with (hole via) and (attach\$3 mounting mount) with (board substrate) with (chip ic semiconductor \$5electronic electr\$4) near2 (component device element) same solder\$3 and (fill\$3 dispens\$3) with solder with (paste ink) with (hole via)	USPAT; EPO; JPO	OR	ON	2006/09/08 18:55
L4	0	((print\$3 wiring circuit) near3 (board substrate) and (attach\$3 mounting mount juxtapos\$3) with (chip ic semiconductor \$5electronic electr\$4) and (fill\$3 dispens\$3) with (metal\$3 conduct\$3 solder) with (paste ink) with (hole via) and (without not no) near3 seed\$3).clm.	US-PGPUB; USPAT; EPO; JPO	OR	ON	2006/09/08 18:58
L5	0	(print\$3 wiring circuit) near3 (board substrate) and (attach\$3 mounting mount juxtapos\$3) with (chip ic semiconductor \$5electronic electr\$4) and (fill\$3 dispens\$3) with (metal\$3 conduct\$3 solder) with (paste ink) with (hole via) and (without not no) near3 seed\$3	US-PGPUB; USPAT; EPO; JPO	OR	ON	2006/09/08 18:59
L6	44	((print\$3 wiring circuit) near3 (board substrate) and (attach\$3 mounting mount juxtapos\$3) with (chip ic semiconductor \$5electronic electr\$4) and (fill\$3 dispens\$3) with (metal\$3 conduct\$3 solder) with (paste ink) with (hole via)).clm.	US-PGPUB; USPAT; EPO; JPO	OR	ON	2006/09/08 18:59
S1	2	microelectronic.ti. and jamil.in.	US-PGPUB; USPAT; EPO; JPO	OR	ON	2006/09/08 18:51
S78	65	(print\$3 wiring circuit) near3 (board substrate) with (hole via) and (attach\$3 mounting) with (board substrate) with (chip ic semiconductor (\$5electronic electrical) near2 (component device element)) same solder\$3 and (fill\$3 dispens\$3) with solder with (paste ink) with (hole via)	USPAT; EPO; JPO	OR	ON	2006/09/08 18:53